JAN 2 3 2004 J

THE UNITED STATES PATENT AND TRADEMARK OFFICE

re: Application of REN, et al.

Serial No.:

10/649,712

Examiner:

Date Filed:

August 26, 2003

Group: 2812

For:

GAN-TYPE ENHANCEMENT MOSFET USING HERETO STRUCTURE

CERTIFICATE UNDER 37 CFR 1.8(a)

I hereby certify that this correspondence is being deposited with the U.S. Postal Service as First Class mail in an envelope addressed to the Commissioner for

Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on

Neil R. Jetter, Es

Reg. No. 46,803

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT PURSUANT TO 37 C.F.R. § 1.97(b)(3)

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

In accordance with the duty of disclosure under 37 C.F.R. § 1.56, Applicants hereby notify the U.S. Patent and Trademark Office of the documents which are listed on the attached Forms PTO/SB/08A and PTO/SB/08B which the Examiner may deem relevant to patentability of the claims of the above-identified application. These references are being timely filed in accordance with 37 CFR § 1.97(b)(3)

The submission of the listed documents is not intended as an admission that any listed document constitutes prior art against the claims of the present application. Applicants do not waive any right to take any action that would be appropriate to antedate or otherwise remove any listed document as a competent reference against the claims of the present application.

Applicants respectfully request that the listed documents be considered by the Examiner and be made of record in the present application and that an initialed copy of Form PTO/SB/08A & B be returned in accordance with MPEP §609.

Although no fee is believed due, the Commissioner is hereby authorized to charge any fees

which may be required by submission of these papers to Deposit Account No. 50-0951.

Dated:

Respectfully submitted,

AKERMAN SENTERFITT

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